



HammerBlade: A Supercomputer for ML & Graphs

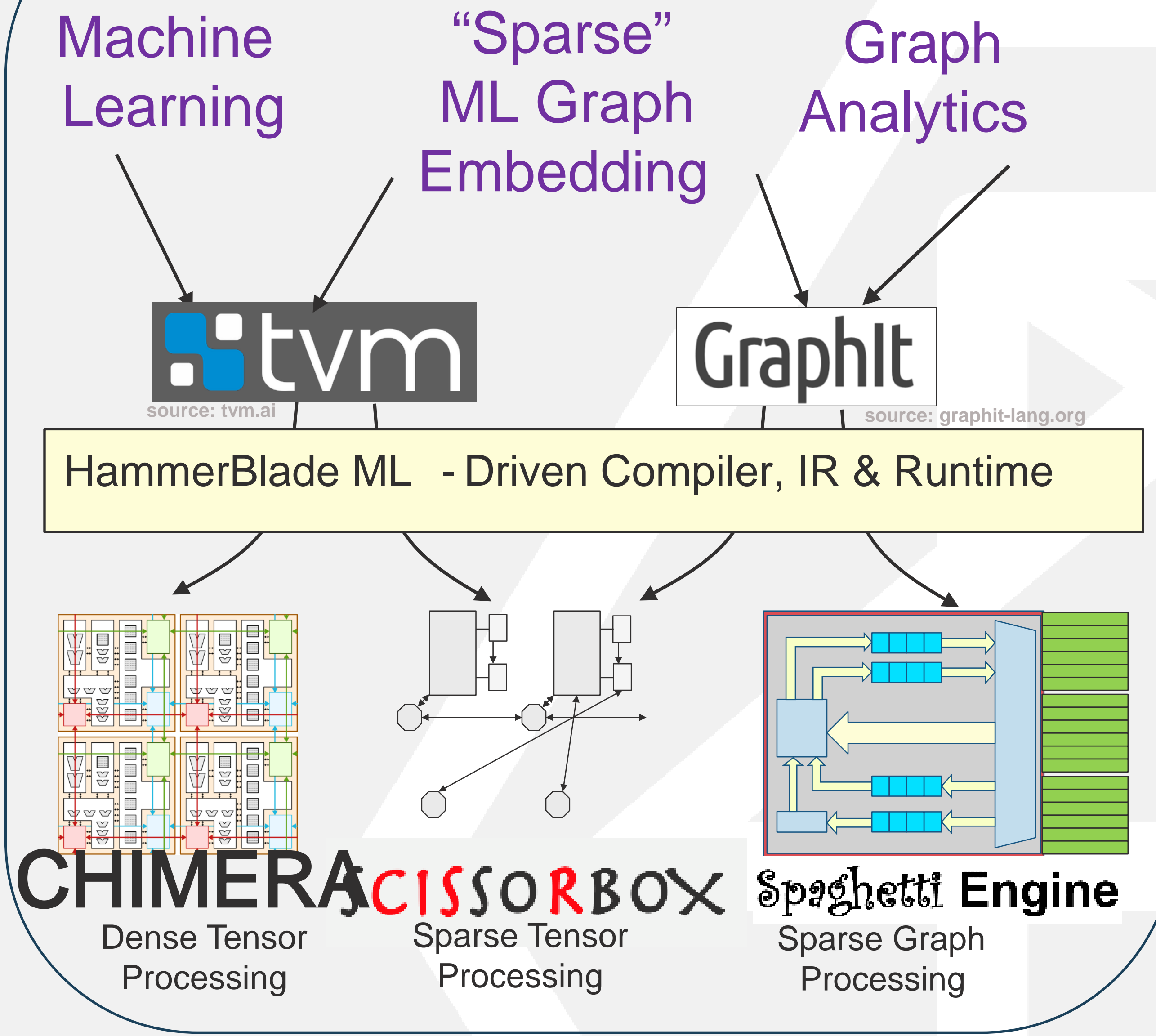
Michael Taylor¹, Luis Ceze¹, Mark Oskin¹, Adrian Sampson², Chris Batten², Zhiru Zheng²
¹University of Washington, ²Cornell University

Specialized Functions: Software Defined Hardware (SDH)

Defining Challenges

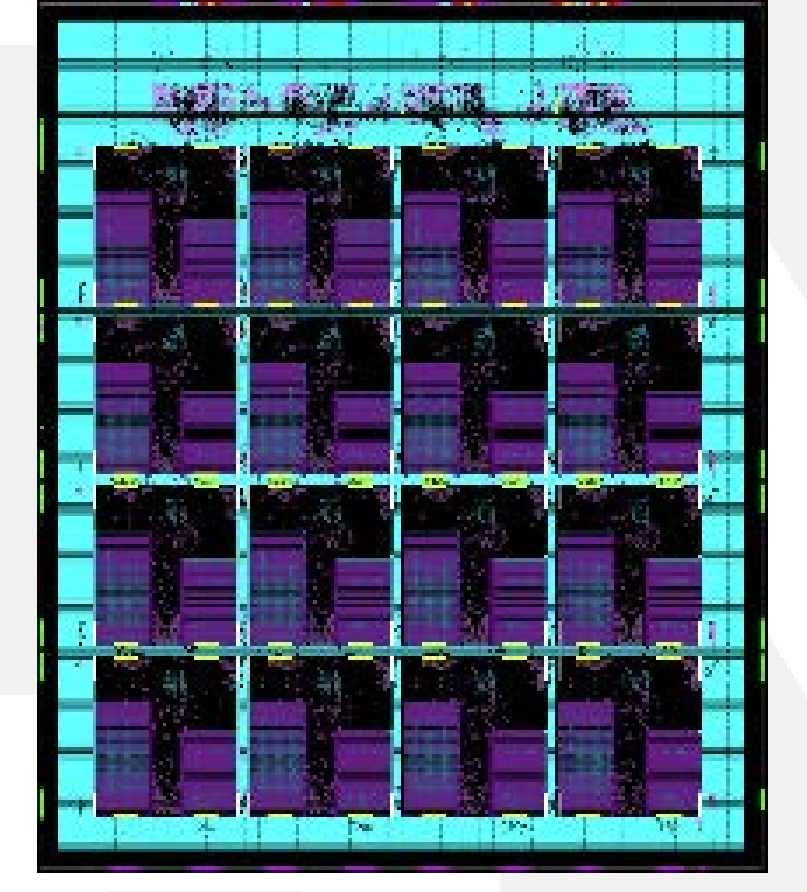
ML workloads are rapidly evolving but require extreme specialization to be feasible.
For example, ML is being combined with Graph computations to create new classes of revolutionary algorithms.
New architectures must attain this specialization while remaining sufficiently flexible.

Software Architecture



Accomplishments

Pytorch/TVM & GraphIt Codes Running On Emulated HW
tvm
GraphIt



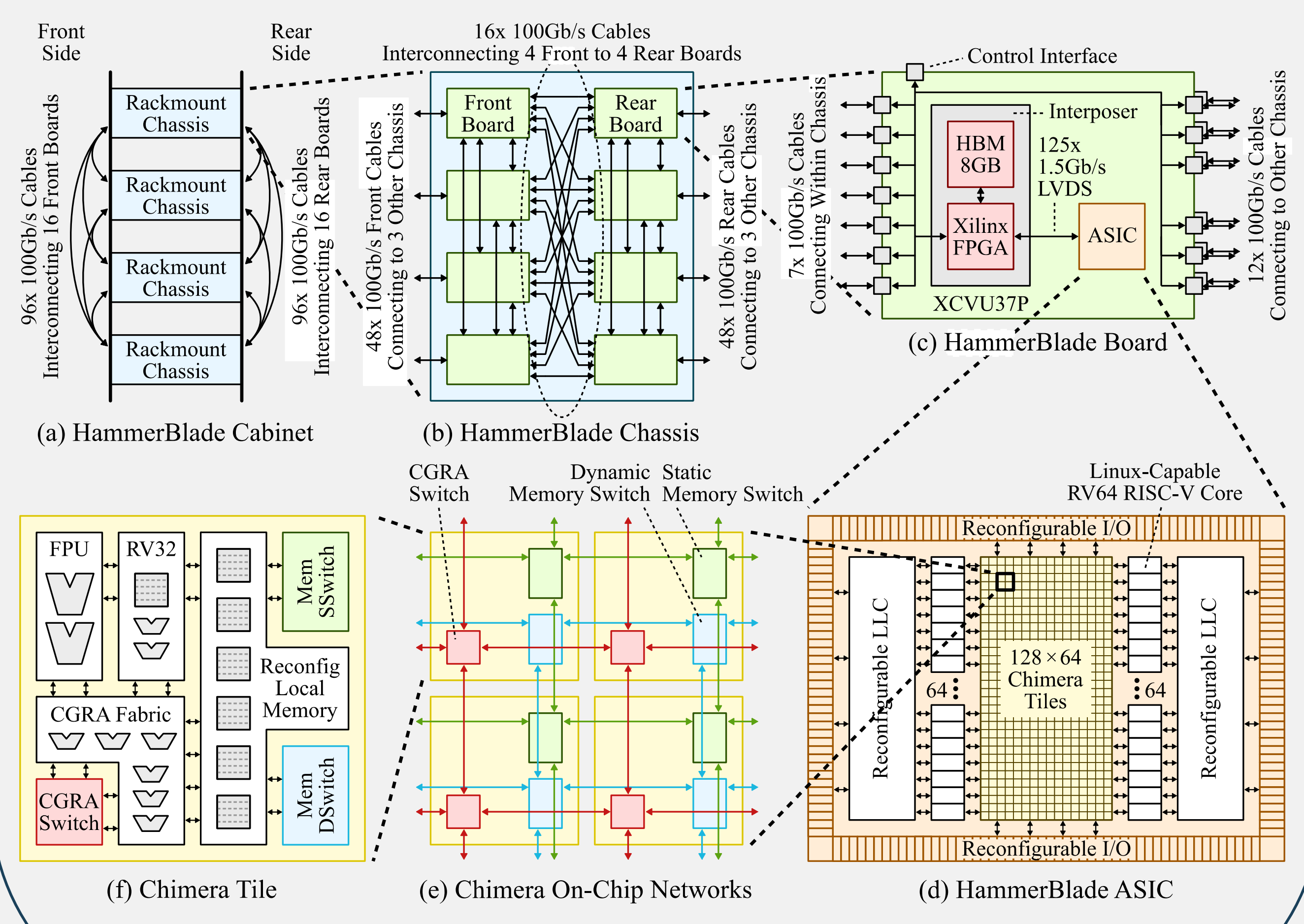
GF 14nm Silicon Tapein

Anticipated Impact

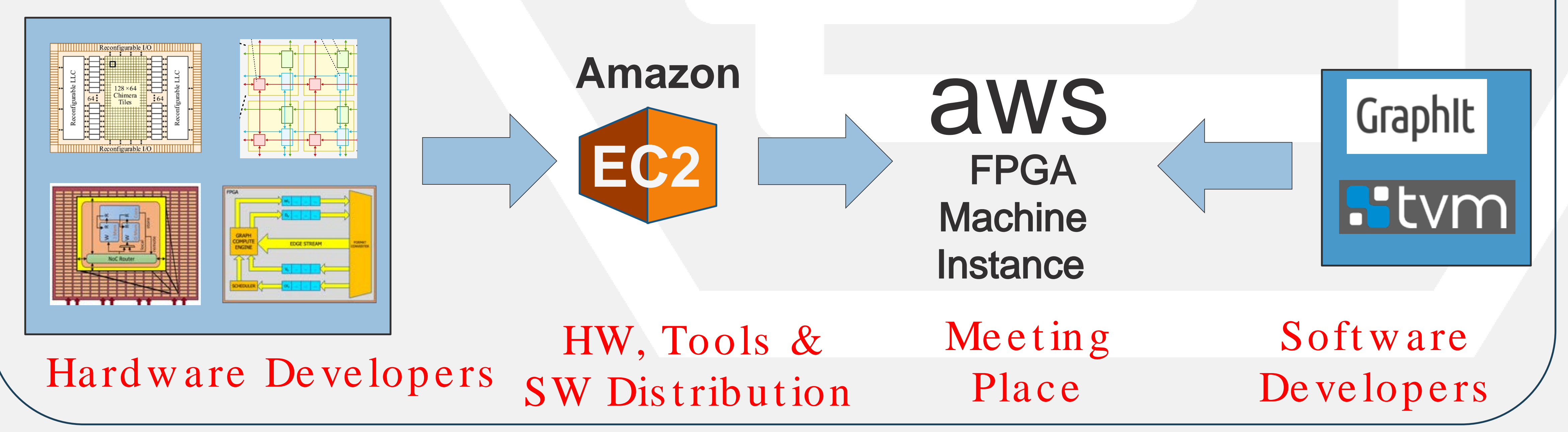
Improves energy efficiency by 300X
A HW Architecture that is future-proofed for rapidly evolving ML/Graph hybrid workloads.

Hardware Architecture

Overall Goal: Gate-to-Rack Scale HW & SW Specialization



HW/SW Transition Model Using Amazon Cloud FPGAs



This research was developed with funding from the Defense Advanced Research Projects Agency (DARPA). The views, opinions and/or findings expressed are those of the author and should not be interpreted as representing the official values or policies of the Department of Defense or the U.S. Government.